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Substitute for form 1449A/PTO


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Application Number	09/935,779
Filing Date	August 24, 2001
First Named Inventor	Frampton E. ELLIS III
Art Unit	2153
Examiner Name	Sean M. Reilly
Attorney Docket Number	313449-P0008

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U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
Examiner Signature				Date Considered	11/12/07	

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Substitute for form 1449A/PTO			Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	09/935,779	
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
		Waltz et al., "Make 'em pay: Billing Net Usage", MacWeek, Vol. 6 (No. 27), p. 24 (2) (Dialog full text), (July 27, 1992).		
		"The Economics of Network Billing: Network Billing and Monitoring Systems Can Improve Efficiency and Cut Costs", IBM System User, Vol. 14 (No. 11), p. 53 (1) (Dialog Fulltext), (Nov., 1993).		
		"Let Your Computer Make Money While You Sleep", Newsbytes, p. 1 (Dialog Fulltext), (August 16, 1996).		
		Regenold et al., "A Single-Chip Multiprocessor DSP Solution for Communication Applications", ASIC Conference and Exhibit 1994, pp. 437-440.		
		Geppert, L., "Solid State [Trend/Development]", IEEE Spectrum, Vol. 33, Issue 1, 1996, pp. 51-55.		
		Li, Yao, "Free-Space Optical Bus-based WDMA Interconnects for Parallel Computation", LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, pp. 588-589, November 16-19, 1992.		
		Dickinson et al., "An Integrated Free Space Optical Bus", 1989 IEEE International Conference on Computer Design, VLSI In Computers and Processors, pp. 62-65, October 2-4, 1989.		
		Natarajan et al., "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor", Journal of Lightwave Technology, Vol. 13, No. 6, pp. 1031-1040, June 6, 1995.		
		Zhao et al., "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems", Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, pp. 188-195, October 23-24, 1995.		
		Wu et al., "Microprocessor Control Signal Transmission Through Optical Fiber", Conference Record of 1992, IEEE Industry Applications Society Annual Meeting, pp. 1747-1750, October 4-9, 1992.		
Examiner Signature			Date Considered	1/17/02


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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SL		NONE, "PC Vision: Intel unveils plans to bring PCs to Vehicles"; EDGE: Work-Group Computing Report, EDGE Publishing, pp. 1-2 (October 28, 1996).	
BL		The American Heritage College Dictionary 4th Ed. - definition of "firewall".	
W		White, "Covert Distributed Processing with Computer Viruses", Advances in Cryptology, Crypto 89, Springer LNCS, Vol. 435, pp. 616-619.	
W		Wilkinson et al., "Parallel Programming", Prentice Hall, 1998.	
SL		Slater, "The Microprocessor Today", IEEE Micro 1996, pp. 32-44.	
Examiner Signature			Date Considered
			1/07/07

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